

Jun. 15. 2004 2:58PM Haynes Beffel &amp; Wolfeld LLP

No. 0756 P. 1

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I hereby certify that this correspondence is being faxed to: Commissioner for Patents,  
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 Paula Faulk Hurley

15 June 2004

**ATTY DOCKET: MXIC 1555-1****IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

<b>In re Application</b> <b>Inventor(s):</b> Shu-Sing Liao <b>Appl. No.:</b> 10/801,954 <b>Confirm. No.:</b> 2359 <b>Filed:</b> 16 March 2004 <b>Title:</b> Method and System for Analyzing Defects of an Integrated Circuit Wafer	<b>Group Art Unit:</b> 2812 <b>Examiner:</b> Unassigned  <b>Customer No. 22470</b>
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Commissioner for Patents  
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**REQUEST FOR CORRECTED FILING RECEIPT**

Sir:

Attached is a marked-up copy of the official Filing Receipt to identify the correction needed.

The correction is as follows:

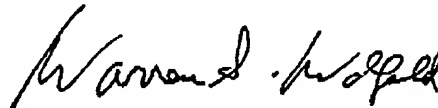
Please change the country of the first inventor from CHINA to **REPUBLIC OF CHINA**.

Please change the country of the second inventor from CHINA to **REPUBLIC OF CHINA**.

Please change the location of the Assignee from Hsinchu, CHINA to **Hsinchu, Taiwan, REPUBLIC OF CHINA**.

Since the error was not caused by applicant, but by the Patent Office, no fee is believed necessary. However, the Commissioner is hereby authorized to charge any additional fees that may be required by this paper, or to credit any overpayment to Deposit Account No. 50-0869 (MXIC 1555-1). This paper is submitted in duplicate.

Respectfully submitted,



Warren S. Wolfeld, Reg. No. 31,454

Dated: 15 June 2004

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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/801,954	03/16/2004	2812	1112	MXIC 1555-1	6	39	2

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CONFIRMATION NO. 2359

22470  
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## FILING RECEIPT:



\*OC000000012862503\*

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## Applicant(s)

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*Republic of China*

## Assignment For Published Patent Application

Macronix International Co., Ltd., Hsinchu, CHINA;

*Hsinchu, Taiwan,*  
*Republic of China*

Domestic Priority data as claimed by applicant

## Foreign Applications

If Required, Foreign Filing License Granted: 06/03/2004

Projected Publication Date: 09/22/2005

Non-Publication Request: No

HAYNES BEFFEL &amp; WOLFELD LLP

Early Publication Request: No

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## Title

Method and system for analyzing defects of an integrated circuit wafer

**Preliminary Class**

438

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